Contents

Section 1 Overview 1

Section 2 Lexical conventions 5

Section 3 Data types 13

Section 4 Expressions 27

Section 5 Scheduling semantics 45

Section 6 Assignments 50

Section 7 Gate and switch level modeling 55

Section 8 User-defined primitives (UDPs) 87

Section 8 Behavioral modeling 98

Section 10 Tasks and functions 125

Section 11 Disabling of named blocks and tasks 132

Section 12 Hierarchical structures 135

Section 13 Specify blocks 152

Section 14 System tasks and functions 172

Section 15 Value change dump (VCD) file 207

Section 16 Compiler directives 219

Section 17 PLI TF and ACC interface mechanism 228

Section 18 Using ACC routines 234

Section 19 ACC routine definitions 270

Section 20 Using TF routines 444

Section 21 TF routine definitions 449

Section 22 Using VPI routines 525

Section 23 VPI routine definitions 554

Annex A Formal syntax definition 594

Annex B List of keywords 604

Annex C The acc_user.h file 605

Annex D The veriuser.h file 615

Annex E The vpi_user.h file 622

Annex F System tasks and functions 635

Annex G Compiler directives 642

Annex H Bibliography 644